REMARKS

Claims 1-13 will be pending upon entry of the present amendment. Claims 1 and 3-6 are being amended. Claims 8-13 are being newly presented.

Attached hereto is a marked-up version of the changes made to the specification and claims by the current amendment. The attached page is captioned "**Version With Markings to Show Changes Made.**"

All of the claims remaining in the application are now clearly allowable. Favorable consideration and a Notice of Allowance are earnestly solicited.

Respectfully submitted,

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In the Claims:

Claims 1 and 3-6 are being amended as follows:

1.   (Amended) A method of data transfer for use with a signal processor of a modem, ~~including~~comprising:

establishing a program code for executing a data transfer function, the function being divided into phases by inactivity intervals, and the program code including code segments associated with each phase; and

downloading each code segment to a first memory of the processor prior to commencement of the respective phase for execution thereof, ~~characterized in that:~~ each code segment ~~is~~being downloaded only during ~~the~~an associated inactivity interval.

3.   (Amended) A method as claimed in claim 1~~or 2~~, wherein the data transfer function is a modem modulation function.

4.   (Amended) A method as claimed in ~~any one of claims~~claim 1~~to 3~~, wherein the program code is held in a second memory, external of the signal processor.

5.   (Amended) A method as claimed in ~~any one of claims~~claim 1~~to 4~~, wherein the signal processor is in the form of a Datapump.

6.   (Amended) A modem architecture ~~including~~comprising:

a signal processor with ~~a first~~an internal first memory;

a second memory external of the signal processor, wherein the second memory is arranged to hold a program code divided into code segments, for executing phases of a modulation function with inactivity intervals therebetween and the first memory is configured to

sequentially receive the segments downloaded from the second memory to a current segment portion of the first memory for executing same;

~~characterized in that the modem architecture~~<u>wherein the signal processor</u> is programmed to ~~perform the method steps of any one of claims 1 to 5~~<u>establish a program code for executing a data transfer function, the function being divided into phases by inactivity intervals, and the program code including code segments associated with each phase; and download each code segment to the first memory of the signal processor prior to commencement of the respective phase for execution thereof, each code segment being downloaded only during an associated inactivity interval.</u>

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